

Blikani\_4

## Blikani [CPU 313C]

### Blikani

#### General

<b>Name</b>	Blikani	<b>Author</b>	Dipl. Ing. Pavel Votrubec
<b>Comment</b>	Úloha rozblikání žárovky H1 při stisku tlačítka S1. Perioda blikání 2 Hz.	<b>Rack</b>	0
<b>Slot</b>	2		

#### General\Catalog information

<b>Short designation</b>	CPU 313C	<b>Description</b>	Work memory 128KB; 0.1ms/1000 instructions; DI24/DO16; AI5/AO2 integrated; 3 pulse outputs (2.5kHz); 3 channels counting and measuring with 24V (30kHz) incremental encoders; MPI interface; multi-tier configuration up to 31 modules
<b>Order number</b>	6ES7 313-5BG04-0AB0	<b>Firmware version</b>	V3.3

#### General\Identification & Maintenance

<b>Plant designation</b>		<b>Location identifier</b>	
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#### MPI interface\General

<b>Name</b>	MPI interface_1	<b>Comment</b>	
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#### MPI interface\General\Catalog information

<b>Short designation</b>	DI24/DO16	<b>Description</b>	Digital input/output DI24 + DO16
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#### MPI interface\Interrupt selection

<b>Interrupt selection</b>	None		
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#### MPI interface\Inputs

<b>Temperature unit</b>	Degrees Celsius		
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#### MPI interface\Inputs\Channel group 0 - 3

<b>Input delay</b>	3ms		
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#### MPI interface\Inputs\Channel group 0 - 3\Hardware interrupt channel 0

<b>Rising (positive) edge</b>	0	<b>Falling (negative) edge</b>	0
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#### MPI interface\Inputs\Channel group 0 - 3\Hardware interrupt channel 1

<b>Rising (positive) edge</b>	0	<b>Falling (negative) edge</b>	0
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#### MPI interface\Inputs\Channel group 0 - 3\Hardware interrupt channel 2

<b>Rising (positive) edge</b>	0	<b>Falling (negative) edge</b>	0
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#### MPI interface\Inputs\Channel group 0 - 3\Hardware interrupt channel 3

<b>Rising (positive) edge</b>	0	<b>Falling (negative) edge</b>	0
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#### MPI interface\Inputs\Channel group 4 - 7

<b>Input delay</b>	3ms		
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#### MPI interface\Inputs\Channel group 4 - 7\Hardware interrupt channel 4

<b>Rising (positive) edge</b>	0	<b>Falling (negative) edge</b>	0
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#### MPI interface\Inputs\Channel group 4 - 7\Hardware interrupt channel 5

<b>Rising (positive) edge</b>	0	<b>Falling (negative) edge</b>	0
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#### MPI interface\Inputs\Channel group 4 - 7\Hardware interrupt channel 6

<b>Rising (positive) edge</b>	0	<b>Falling (negative) edge</b>	0
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#### MPI interface\Inputs\Channel group 4 - 7\Hardware interrupt channel 7

<b>Rising (positive) edge</b>	0	<b>Falling (negative) edge</b>	0
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#### MPI interface\Inputs\Channel 0

<b>Measuring type</b>	Voltage	<b>Measuring range</b>	+/- 10V
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Interference frequency suppression	50Hz	Integration time	20ms
<b>MPI interface\Inputs\Channel group 8 - 11</b>			
Input delay	3ms		
<b>MPI interface\Inputs\Channel group 8 - 11\Hardware interrupt channel 8</b>			
Rising (positive) edge	0	Falling (negative) edge	0
<b>MPI interface\Inputs\Channel group 8 - 11\Hardware interrupt channel 9</b>			
Rising (positive) edge	0	Falling (negative) edge	0
<b>MPI interface\Inputs\Channel group 8 - 11\Hardware interrupt channel 10</b>			
Rising (positive) edge	0	Falling (negative) edge	0
<b>MPI interface\Inputs\Channel group 8 - 11\Hardware interrupt channel 11</b>			
Rising (positive) edge	0	Falling (negative) edge	0
<b>MPI interface\Inputs\Channel 1</b>			
Measuring type	Voltage	Measuring range	+/- 10V
Interference frequency suppression	50Hz	Integration time	20ms
<b>MPI interface\Inputs\Channel group 12 - 15</b>			
Input delay	3ms		
<b>MPI interface\Inputs\Channel group 12 - 15\Hardware interrupt channel 12</b>			
Rising (positive) edge	0	Falling (negative) edge	0
<b>MPI interface\Inputs\Channel group 12 - 15\Hardware interrupt channel 13</b>			
Rising (positive) edge	0	Falling (negative) edge	0
<b>MPI interface\Inputs\Channel group 12 - 15\Hardware interrupt channel 14</b>			
Rising (positive) edge	0	Falling (negative) edge	0
<b>MPI interface\Inputs\Channel group 12 - 15\Hardware interrupt channel 15</b>			
Rising (positive) edge	0	Falling (negative) edge	0
<b>MPI interface\Inputs\Channel 2</b>			
Measuring type	Voltage	Measuring range	+/- 10V
Interference frequency suppression	50Hz	Integration time	20ms
<b>MPI interface\Inputs\Channel group 16 - 19</b>			
Input delay	3ms		
<b>MPI interface\Inputs\Channel group 16 - 19\Hardware interrupt channel 16</b>			
Rising (positive) edge	0	Falling (negative) edge	0
<b>MPI interface\Inputs\Channel group 16 - 19\Hardware interrupt channel 17</b>			
Rising (positive) edge	0	Falling (negative) edge	0
<b>MPI interface\Inputs\Channel group 16 - 19\Hardware interrupt channel 18</b>			
Rising (positive) edge	0	Falling (negative) edge	0
<b>MPI interface\Inputs\Channel group 16 - 19\Hardware interrupt channel 19</b>			
Rising (positive) edge	0	Falling (negative) edge	0
<b>MPI interface\Inputs\Channel 3</b>			
Measuring type	Voltage	Measuring range	+/- 10V
Interference frequency suppression	50Hz	Integration time	20ms
<b>MPI interface\Inputs\Channel group 20 - 23</b>			
Input delay	3ms		

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<b>MPI interface\Inputs\Channel group 20 - 23\Hardware interrupt channel 20</b>								
Rising (positive) edge	0	Falling (negative) edge	0					
<b>MPI interface\Inputs\Channel group 20 - 23\Hardware interrupt channel 21</b>								
Rising (positive) edge	0	Falling (negative) edge	0					
<b>MPI interface\Inputs\Channel group 20 - 23\Hardware interrupt channel 22</b>								
Rising (positive) edge	0	Falling (negative) edge	0					
<b>MPI interface\Inputs\Channel group 20 - 23\Hardware interrupt channel 23</b>								
Rising (positive) edge	0	Falling (negative) edge	0					
<b>MPI interface\Inputs\Channel 4</b>								
Measuring type	Resistor (2-wire terminal)	Measuring range	600 ohmsOhm					
<b>MPI interface\MPI address\Interface networked with</b>								
Subnet:	Not networked							
<b>MPI interface\MPI address\Parameters</b>								
Address:	2	Highest address:	31					
Transmission speed:	187.5 kbps							
<b>MPI interface\I/O addresses\Input addresses</b>								
Start address	0	End address	2					
Process image	OB1-PI	Interrupt OB number	40					
<b>MPI interface\I/O addresses\Output addresses</b>								
Start address	0	End address	1					
Process image	OB1-PI							
<b>MPI interface\Channel 0</b>								
Operating mode	Not configured							
<b>MPI interface\Outputs\Output 0</b>								
Output type	Voltage	Output range	+/- 10V					
<b>MPI interface\Outputs\Output 1</b>								
Output type	Voltage	Output range	+/- 10V					
<b>MPI interface\Channel 1</b>								
Operating mode	Not configured							
<b>MPI interface\Channel 2</b>								
Operating mode	Not configured							
<b>IO address overview</b>								
outputs	true	inputs	true					
outputs	true	outputs	true					
Type	AddrFrom	AddrTo	Module	PIP	DP	PN	Rack	Slot
true	true	true	true	true	true	true	true	true
<b>Startup</b>								
Startup if preset configuration does not match actual configuration	True		Startup after POWER ON	Warm restart				
<b>Startup\Monitoring time for</b>								
Ready message from modules	650x 100 ms		Parameter transfer to modules	100x 100 ms				
<b>Cycle</b>								
Cycle monitoring time	150ms		Cycle load due to communication	20%				
Size of the process image input:	128		Size of the process image output:	128				
OB85 call if I/O access error occurs	No OB85 call							
<b>Clock memory</b>								
Memory byte	0							
<b>Clock memory\Clock memory</b>								
Clock memory	False							

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<b>\Time-of-day interrupts\</b>			
<b>OB number</b>	<b>Priority</b>	<b>Activated</b>	<b>Interval</b>
OB 10:	2	False	None
			<b>Start time</b>
			1994-01-01 00:00:00.000
<b>\Time-delay interrupts\</b>			
<b>OB number</b>	<b>Priority</b>	<b>Process image partition(s)</b>	
OB 20:	3	None	
OB 21:	4	None	
<b>\Cyclic interrupts\</b>			
<b>OB number</b>	<b>Priority</b>	<b>Interval</b>	<b>Phase offset</b>
OB 32:	9	1000	0
			ms
OB 33:	10	500	0
			ms
OB 34:	11	200	0
			ms
OB 35:	12	100	0
			ms
<b>\Hardware interrupts\</b>			
<b>OB number</b>	<b>Priority</b>		
OB 40:	16		
<b>\Asynchronous error interrupts\</b>			
<b>OB number</b>	<b>Priority</b>		
OB 82:	26		
OB 85:	26		
OB 87:	26		
<b>Diagnostics system</b>			
<b>Number of alarms in the diagnostics buffer</b>	10		
<b>Diagnostics system\Report cause of STOP</b>			
<b>Report cause of STOP</b>	True		
<b>System diagnostics\General</b>			
<b>Activate system diagnostics for this PLC</b>	False		
<b>Time of day</b>			
<b>Correction factor</b>	0ms		
<b>Time of day\Synchronization on PLC</b>			
<b>Type of synchronization</b>	None	<b>Time interval</b>	None
<b>Time of day\Synchronization on MPI</b>			
<b>Type of synchronization</b>	None	<b>Time interval</b>	None
<b>Retentive memory\</b>			
<b>Number of memory bytes starting at MB 0</b>	16	<b>Number of S7 timers starting at T 0</b>	0
<b>Number of S7 counters starting at C 0</b>	8		
<b>Protection\</b>			
<b>Level of protection</b>	No protection		
<b>Protection\ \Can be canceled with password</b>			
<b>Can be canceled with password</b>	False		
<b>Protection\Password for read/write access</b>			
<b>Password</b>	••••••	<b>Confirm password</b>	••••••
<b>Anchor (ParameterCommunicationMenu)</b>			
<b>The TreeNode ParameterCommunicationMenu was not filled by some ACF</b>			

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**Anchor (AddressesOverviewMenu)**

The AddressesOver-  
viewMenu was not fil-  
led by some ACF

## Blikani [CPU 313C] / Program blocks

### Block\_1 [FB1]

#### Block\_1 Properties

##### General

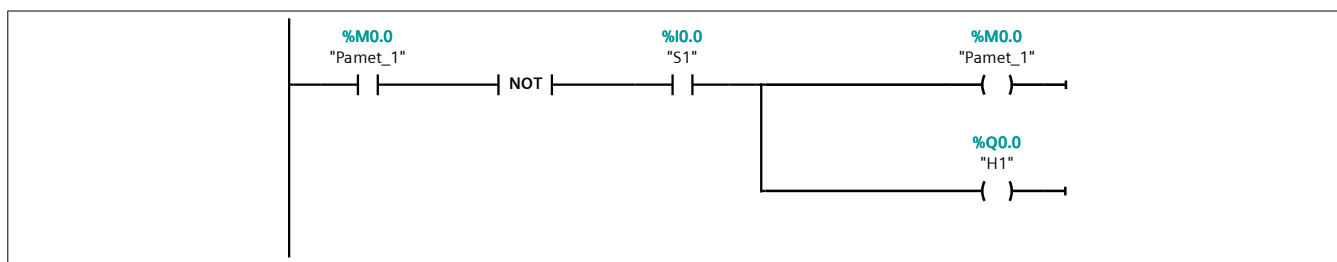
Name	Block_1	Number	1	Type	FB
Language	LAD				

##### Information

Title	funkční blok	Author	Pavel	Comment	Při stisknutí tlačítka S1 se rozblíká žárovka H1. Využívá časového zápisu do paměťového místa M0.0 a jeho negace při dalším spuštění. Svícení je podmíněné stisknutím tlačítka S1. Perioda je nastavena systémově v HW procesu OB33.
Family		Version	0.1	User-defined ID	

Name	Data type	Offset	Default value	Retain	Accessible from HMI	Visible in HMI	Set-point	Comment
▼ Input								
Input_1	Bool	0.0	false	Set in IDB	True	True	False	
▼ Output								
Output_1	Bool	2.0	false	Set in IDB	True	False	False	
InOut								
Static								
Temp								

#### Network 1:



Symbol	Address	Type	Comment
"S1"	%I0.0	Bool	Čtení stavu vstupu I0.0 tlačítko S1.
"Pamet_1"	%M0.0	Bool	Paměťové místo M0.0
"H1"	%Q0.0	Bool	Zápis na výstup Q0.0 žárovka H1





## Blikani [CPU 313C] / Program blocks

### CYC\_INT3 [OB33]

#### CYC\_INT3 Properties

##### General

<b>Name</b>	CYC_INT3	<b>Number</b>	33	<b>Type</b>	OB
<b>Language</b>	STL				

##### Information

<b>Title</b>	"Cyclic Interrupt"	<b>Author</b>		<b>Comment</b>	Časově spouštěný blok OB33. Perioda spuštění bloku 500 ms.
<b>Family</b>		<b>Version</b>	0.1	<b>User-defined ID</b>	

Name	Data type	Offset	Comment
▼ Temp			
OB33_EV_CLASS	Byte	0.0	Bits 0-3 = 1 (Coming event), Bits 4-7 = 1 (Event class 1)
OB33_STRT_INF	Byte	1.0	16#34 (OB 33 has started)
OB33_PRIORITY	Byte	2.0	Priority of OB Execution
OB33_OB_NUMBR	Byte	3.0	33 (Organization block 33, OB33)
OB33_RESERVED_1	Byte	4.0	Reserved for system
OB33_RESERVED_2	Byte	5.0	Reserved for system
OB33_PHS_OFFSET	Int	6.0	Phase offset (integer, milliseconds)
OB33_RESERVED_3	Int	8.0	Reserved for system
OB33_EXC_FREQ	Int	10.0	Frequency of execution (msec)
OB33_DATE_TIME	Date_And_Time	12.0	Date and time OB33 started

#### Network 1:

```

0001      CALL  "Block_1", "Block_1_DB"
0002          Input_1  :=
0003          Output_1 :=
0004

```

Symbol	Address	Type	Comment
"Block_1"	%FB1	Block_FB	
"Block_1_DB"	%DB1	Block_FB	




## Blikani [CPU 313C]

### Technology objects

This folder is empty.

## Blikani [CPU 313C] / PLC tags / Default tag table [3]

### PLC tags

PLC tags							
Name	Data type	Address	Retain	Visible in HMI	Accessible from HMI	Comment	
 S1	Bool	%I0.0		True	True	Čtení stavu vstupu I0.0 tlačítko S1.	
 H1	Bool	%Q0.0		True	True	Zápis na výstup Q0.0 žárovka H1	
 Pamet_1	Bool	%M0.0		True	True	Paměťové místo M0.0	

## Blikani [CPU 313C] / PLC tags / Default tag table [3]

### User constants

#### User constants

Name	Data type	Value	Comment
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## Blikani [CPU 313C]

### PLC data types

This folder is empty.

## Blikani [CPU 313C] / Watch and force tables

### Force table

Name	Address	Display format	Force value	Comment
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## Blikani [CPU 313C] / PLC alarms

### PLC alarms

#### PLC alarms

no entries

## Blikani [CPU 313C] / PLC alarms

### User diagnostics alarms

#### User diagnostics alarms

no entries



## Blikani [CPU 313C] / PLC alarms

### System diagnostics alarms

#### System diagnostics alarms

no entries

## Blikani [CPU 313C]

### Text lists

This folder is empty.

## Blikani [CPU 313C] / Local modules

### CP 343-2\_1 [CP 343-2]

#### CP 343-2\_1

##### General

<b>Name</b>	CP 343-2_1	<b>Author</b>	student06
<b>Comment</b>		<b>Rack</b>	0
<b>Slot</b>	4		

##### General\Catalog information

<b>Short designation</b>	CP 343-2	<b>Description</b>	Firmware V3.0/V3.1. Basic module for AS-i attachment. Support of AS-i A/B slaves and AS-i-7.3/7.4 analog slaves.
<b>Order number</b>	6GK7 343-2AH01-0XA0	<b>Firmware version</b>	V3.1

##### I/O addresses\Input addresses

<b>Start address</b>	256	<b>End address</b>	271
<b>Process image</b>	None		

##### I/O addresses\Output addresses

<b>Start address</b>	256	<b>End address</b>	271
<b>Process image</b>	None		

##### Anchor (AsiCMAAddressesOverviewMenu)

I address	O address	AS-i address
		0
256.0 ...256.3	256.0 ...256.3	1A
257.4 ...257.7	257.4 ...257.7	2A
257.0 ...257.3	257.0 ...257.3	3A
258.4 ...258.7	258.4 ...258.7	4A
258.0 ...258.3	258.0 ...258.3	5A
259.4 ...259.7	259.4 ...259.7	6A
259.0 ...259.3	259.0 ...259.3	7A
260.4 ...260.7	260.4 ...260.7	8A
260.0 ...260.3	260.0 ...260.3	9A
261.4 ...261.7	261.4 ...261.7	10A
261.0 ...261.3	261.0 ...261.3	11A
262.4 ...262.7	262.4 ...262.7	12A
262.0 ...262.3	262.0 ...262.3	13A
263.4 ...263.7	263.4 ...263.7	14A
263.0 ...263.3	263.0 ...263.3	15A
264.4 ...264.7	264.4 ...264.7	16A
264.0 ...264.3	264.0 ...264.3	17A
265.4 ...265.7	265.4 ...265.7	18A
265.0 ...265.3	265.0 ...265.3	19A
266.4 ...266.7	266.4 ...266.7	20A
266.0 ...266.3	266.0 ...266.3	21A
267.4 ...267.7	267.4 ...267.7	22A
267.0 ...267.3	267.0 ...267.3	23A
268.4 ...268.7	268.4 ...268.7	24A
268.0 ...268.3	268.0 ...268.3	25A
269.4 ...269.7	269.4 ...269.7	26A
269.0 ...269.3	269.0 ...269.3	27A
270.4 ...270.7	270.4 ...270.7	28A
270.0 ...270.3	270.0 ...270.3	29A
271.4 ...271.7	271.4 ...271.7	30A
271.0 ...271.3	271.0 ...271.3	31A