

Plnicka_vin

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PLC_1 [CPU 314C-2 DP]

PLC_1

General

Name	PLC_1	Author	ucitel
Comment		Rack	0
Slot	2		

General\Catalog information

Short designation	CPU 314C-2 DP	Description	Work memory 96KB; 0.1ms/1000 instructions; DI24/DO16; AI5/AO2 integrated; 4 pulse outputs (2.5kHz); 4 channels counting and measuring with 24 V (60kHz) incremental encoders; integrated positioning function; MPI+DP interface (DP master or DP slave); multi-tier configuration up to 31 modules; capable of sending and receiving in direct data exchange; constant bus cycle time; routing; S7 communication (loadable FBs/FCs); firmware V2.6; also available as SIPLUS module with order number 6AG1 314-6CG03-2AY0.
Order number	6ES7 314-6CG03-0AB0	Firmware version	V2.6

General\Identification & Maintenance

Plant designation		Location identifier	
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MPI interface\General

Name	MPI interface_1	Comment	
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MPI interface\General\Catalog information

Short designation	DI24/DO16	Description	Digital input/output DI24 + DO16
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MPI interface\Interrupt selection

Interrupt selection	None		
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MPI interface\Interrupt selection

Interrupt selection	None		
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MPI interface\Inputs

Temperature unit	Degrees Celsius		
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MPI interface\Inputs\Channel group 0 - 3

Input delay	3ms		
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MPI interface\Inputs\Channel group 0 - 3\Hardware interrupt channel 0

Rising (positive) edge	0	Falling (negative) edge	0
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MPI interface\Inputs\Channel group 0 - 3\Hardware interrupt channel 1

Rising (positive) edge	0	Falling (negative) edge	0
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MPI interface\Inputs\Channel group 0 - 3\Hardware interrupt channel 2

Rising (positive) edge	0	Falling (negative) edge	0
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MPI interface\Inputs\Channel group 0 - 3\Hardware interrupt channel 3

Rising (positive) edge	0	Falling (negative) edge	0
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MPI interface\Inputs\Channel group 4 - 7

Input delay	3ms		
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MPI interface\Inputs\Channel group 4 - 7\Hardware interrupt channel 4

Rising (positive) edge	0	Falling (negative) edge	0
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MPI interface\Inputs\Channel group 4 - 7\Hardware interrupt channel 5

Rising (positive) edge	0	Falling (negative) edge	0
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Totally Integrated Automation Portal			
MPI interface\Inputs\Channel group 4 - 7\Hardware interrupt channel 6			
Rising (positive) edge	0	Falling (negative) edge	0
MPI interface\Inputs\Channel group 4 - 7\Hardware interrupt channel 7			
Rising (positive) edge	0	Falling (negative) edge	0
MPI interface\Inputs\Channel 0			
Measuring type	Voltage	Measuring range	+/- 10V
Interference frequency suppression	50Hz	Integration time	20ms
MPI interface\Inputs\Channel group 8 - 11			
Input delay	3ms		
MPI interface\Inputs\Channel group 8 - 11\Hardware interrupt channel 8			
Rising (positive) edge	0	Falling (negative) edge	0
MPI interface\Inputs\Channel group 8 - 11\Hardware interrupt channel 9			
Rising (positive) edge	0	Falling (negative) edge	0
MPI interface\Inputs\Channel group 8 - 11\Hardware interrupt channel 10			
Rising (positive) edge	0	Falling (negative) edge	0
MPI interface\Inputs\Channel group 8 - 11\Hardware interrupt channel 11			
Rising (positive) edge	0	Falling (negative) edge	0
MPI interface\Inputs\Channel 1			
Measuring type	Voltage	Measuring range	+/- 10V
Interference frequency suppression	50Hz	Integration time	20ms
MPI interface\Inputs\Channel group 12 - 15			
Input delay	3ms		
MPI interface\Inputs\Channel group 12 - 15\Hardware interrupt channel 12			
Rising (positive) edge	0	Falling (negative) edge	0
MPI interface\Inputs\Channel group 12 - 15\Hardware interrupt channel 13			
Rising (positive) edge	0	Falling (negative) edge	0
MPI interface\Inputs\Channel group 12 - 15\Hardware interrupt channel 14			
Rising (positive) edge	0	Falling (negative) edge	0
MPI interface\Inputs\Channel group 12 - 15\Hardware interrupt channel 15			
Rising (positive) edge	0	Falling (negative) edge	0
MPI interface\Inputs\Channel 2			
Measuring type	Voltage	Measuring range	+/- 10V
Interference frequency suppression	50Hz	Integration time	20ms
MPI interface\Inputs\Channel group 16 - 19			
Input delay	3ms		
MPI interface\Inputs\Channel group 16 - 19\Hardware interrupt channel 16			
Rising (positive) edge	0	Falling (negative) edge	0
MPI interface\Inputs\Channel group 16 - 19\Hardware interrupt channel 17			
Rising (positive) edge	0	Falling (negative) edge	0
MPI interface\Inputs\Channel group 16 - 19\Hardware interrupt channel 18			
Rising (positive) edge	0	Falling (negative) edge	0
MPI interface\Inputs\Channel group 16 - 19\Hardware interrupt channel 19			
Rising (positive) edge	0	Falling (negative) edge	0

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MPI interface\Inputs\Channel 3			
Measuring type	Voltage	Measuring range	+/- 10V
Interference frequency suppression	50Hz	Integration time	20ms
MPI interface\Inputs\Channel group 20 - 23			
Input delay	3ms		
MPI interface\Inputs\Channel group 20 - 23\Hardware interrupt channel 20			
Rising (positive) edge	0	Falling (negative) edge	0
MPI interface\Inputs\Channel group 20 - 23\Hardware interrupt channel 21			
Rising (positive) edge	0	Falling (negative) edge	0
MPI interface\Inputs\Channel group 20 - 23\Hardware interrupt channel 22			
Rising (positive) edge	0	Falling (negative) edge	0
MPI interface\Inputs\Channel group 20 - 23\Hardware interrupt channel 23			
Rising (positive) edge	0	Falling (negative) edge	0
MPI interface\Inputs\Channel 4			
Measuring type	Resistor (2-wire terminal)	Measuring range	600 ohmsOhm
MPI interface\MPI address\Interface networked with			
Subnet:	Not networked		
MPI interface\MPI address\Parameters			
Address:	2	Highest address:	31
Transmission speed:	187.5 kbps		
MPI interface\PROFIBUS address\Interface networked with			
Subnet:	Not networked		
MPI interface\PROFIBUS address\Parameters			
Address:	2	Highest address:	
Transmission speed:	187.5 kbps		
MPI interface\Operating mode\DPOperatingModeInsider\DPOperatingModeOnlyMenu			
Operating mode	DP master	DP master system:	Not created
Assigned DP Master:			
MPI interface\Operating mode\DPOperatingModeInsider\DPOperatingModeAddOnsMenu			
Test, commissioning and routing	False	Watchdog	True
MPI interface\I/O addresses\Input addresses			
Start address	0	End address	2
Process image	OB1-PI	Interrupt OB number	40
MPI interface\I/O addresses\Output addresses			
Start address	0	End address	1
Process image	OB1-PI		
MPI interface\Channel 0			
Operating mode	None	Operating mode	Not configured
MPI interface\Outputs\Output 0			
Output type	Voltage	Output range	+/- 10V
MPI interface\Outputs\Output 1			
Output type	Voltage	Output range	+/- 10V
MPI interface\Time of day\PROFIBUS DP mode			
Synchronization type	None	Update cycle	None
MPI interface\Channel 1			
Operating mode	Not configured		
MPI interface\Channel 2			
Operating mode	Not configured		
MPI interface\Diagnostics addresses\Diagnostics addresses			
Start address	1023		
MPI interface\Channel 3			
Operating mode	Not configured		

Totally Integrated Automation Portal

IO address overview

outputs	true	inputs	true					
outputs	true	outputs	true					
Type	AddrFrom	AddrTo	Module	PIP	DP	PN	Rack	Slot
true	true	true	true	true	true	true	true	true

Startup

Startup if preset configuration does not match actual configuration	True	Startup after POWER ON	Warm restart
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Startup\Monitoring time for

Ready message from modules	650x 100 ms	Parameter transfer to modules	100x 100 ms
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Cycle

Cycle monitoring time	150ms	Cycle load due to communication	20%
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OB85 call if I/O access error occurs	No OB85 call
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Clock memory

Memory byte	0
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Clock memory\Clock memory

Clock memory	False
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\Time-of-day interrupts\

OB number	Priority	Activated	Interval	Start time
OB 10:	2	False	None	1994-01-01 00:00:00.000

\Time-delay interrupts\

OB number	Priority	Process image partition(s)
OB 20:	3	None

\Cyclic interrupts\

OB number	Priority	Interval	Phase offset
OB 35:	12	100	0 ms

\Hardware interrupts\

OB number	Priority	Process image partition(s)
OB 40:	16	None

\Interrupts for DPV1\

OB number	Priority
OB 55:	2
OB 56:	2
OB 57:	2

\Asynchronous error interrupts\

OB number	Priority
OB 82:	26
OB 85:	26
OB 86:	26
OB 87:	26

Diagnostics system

Report cause of STOP	True
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System diagnostics\General

Activate system diagnostics for this PLC	False
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Time of day

Correction factor	0ms
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Time of day\Synchronization on PLC

Type of synchronization	None	Time interval	None
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Totally Integrated Automation Portal			
Time of day\Synchronization on MPI			
Type of synchronization	None	Time interval	None
Operating mode			
	Test mode	Max. cycle time for test functions	5ms
Retentive memory\			
Number of memory bytes starting at MB 0	16	Number of S7 timers starting at T 0	0
Number of S7 counters starting at C 0	8		
Protection\			
Level of protection	Depending on the mode selector setting		
Protection\ \Can be canceled with password			
Can be canceled with password	False		
Protection\Password for read/write access			
Password	••••••••	Confirm password	••••••••
Anchor (ParameterCommunicationMenu)			
The TreeNode ParameterCommunicationMenu was not filled by some ACF			
Anchor (AddressesOverviewMenu)			
The AddressesOverviewMenu was not filled by some ACF			

PLC_1 [CPU 314C-2 DP] / Program blocks

Main [OB1]

Main Properties

General

Name	Main	Number	1	Type	OB
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Language	STL
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Information

Title	"Main Program Sweep (Cycle)"	Author		Comment	
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Family		Version	0.1	User-defined ID	
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Name	Data type	Offset	Comment
▼ Temp			
OB1_EV_CLASS	Byte	0.0	Bits 0-3 = 1 (Coming event), Bits 4-7 = 1 (Event class 1)
OB1_SCAN_1	Byte	1.0	1 (Cold restart scan 1 of OB 1), 3 (Scan 2-n of OB 1)
OB1_PRIORITY	Byte	2.0	Priority of OB Execution
OB1_OB_NUMBR	Byte	3.0	1 (Organization block 1, OB1)
OB1_RESERVED_1	Byte	4.0	Reserved for system
OB1_RESERVED_2	Byte	5.0	Reserved for system
OB1_PREV_CYCLE	Int	6.0	Cycle time of previous OB1 scan (milliseconds)
OB1_MIN_CYCLE	Int	8.0	Minimum cycle time of OB1 (milliseconds)
OB1_MAX_CYCLE	Int	10.0	Maximum cycle time of OB1 (milliseconds)
OB1_DATE_TIME	Date_And_Time	12.0	Date and time OB1 started

Network 1:

```
0001      CALL  "Block_1", "Block_1_DB"
```

Symbol	Address	Type	Comment
"Block_1"	%FB1	Block_FB	
"Block_1_DB"	%DB1	Block_FB	

PLC_1 [CPU 314C-2 DP] / Program blocks

Block_1 [FB1]

Block_1 Properties

General

Name	Block_1	Number	1	Type	FB
Language	LAD				

Information

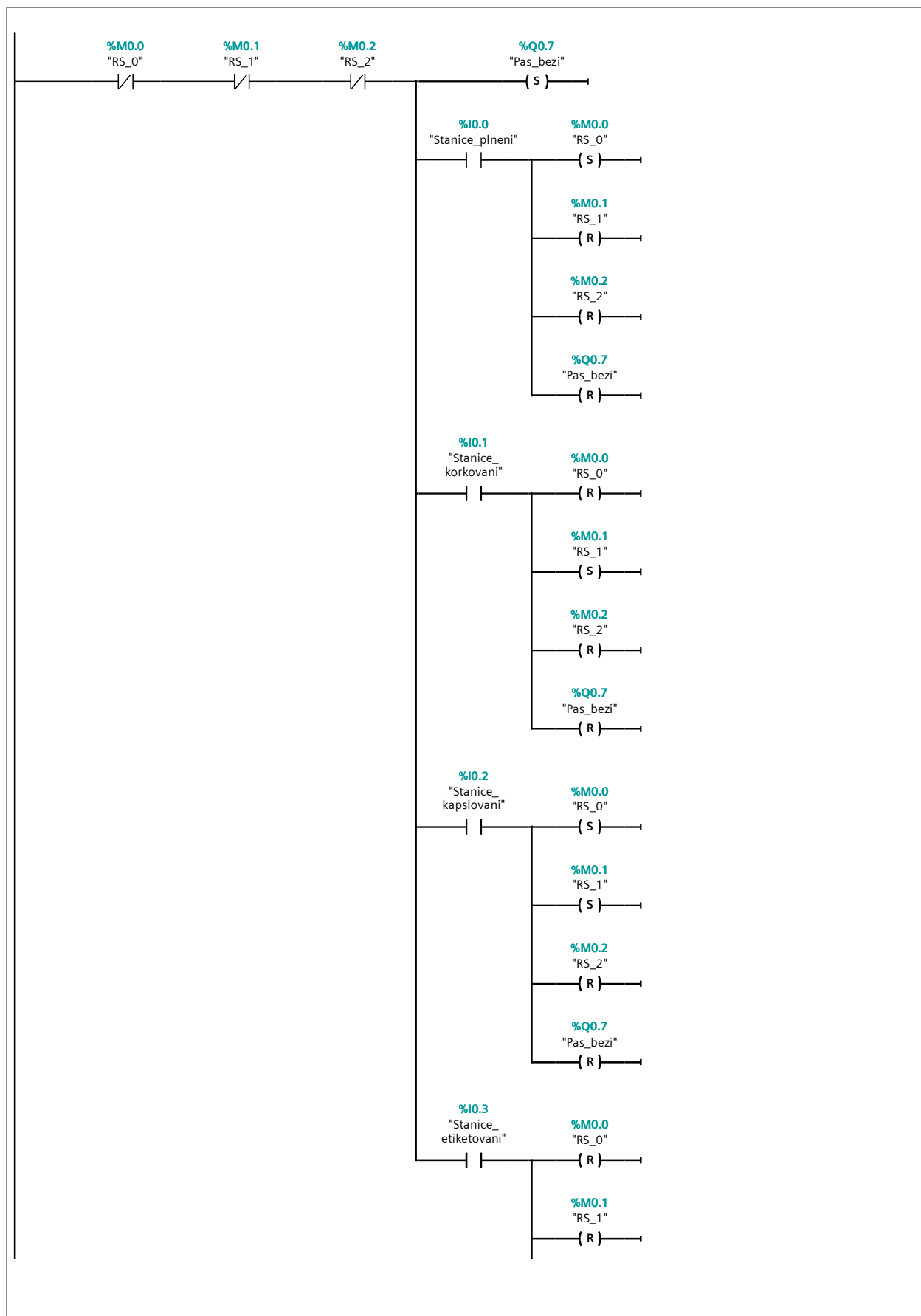
Title		Author		Comment	
Family		Version	0.1	User-defined ID	

Name	Data type	Offset	Default value	Retain	Access- sible from HMI	Visible in HMI	Set- point	Comment
Input								
Output								
InOut								
Static								
Temp								

Network 1:

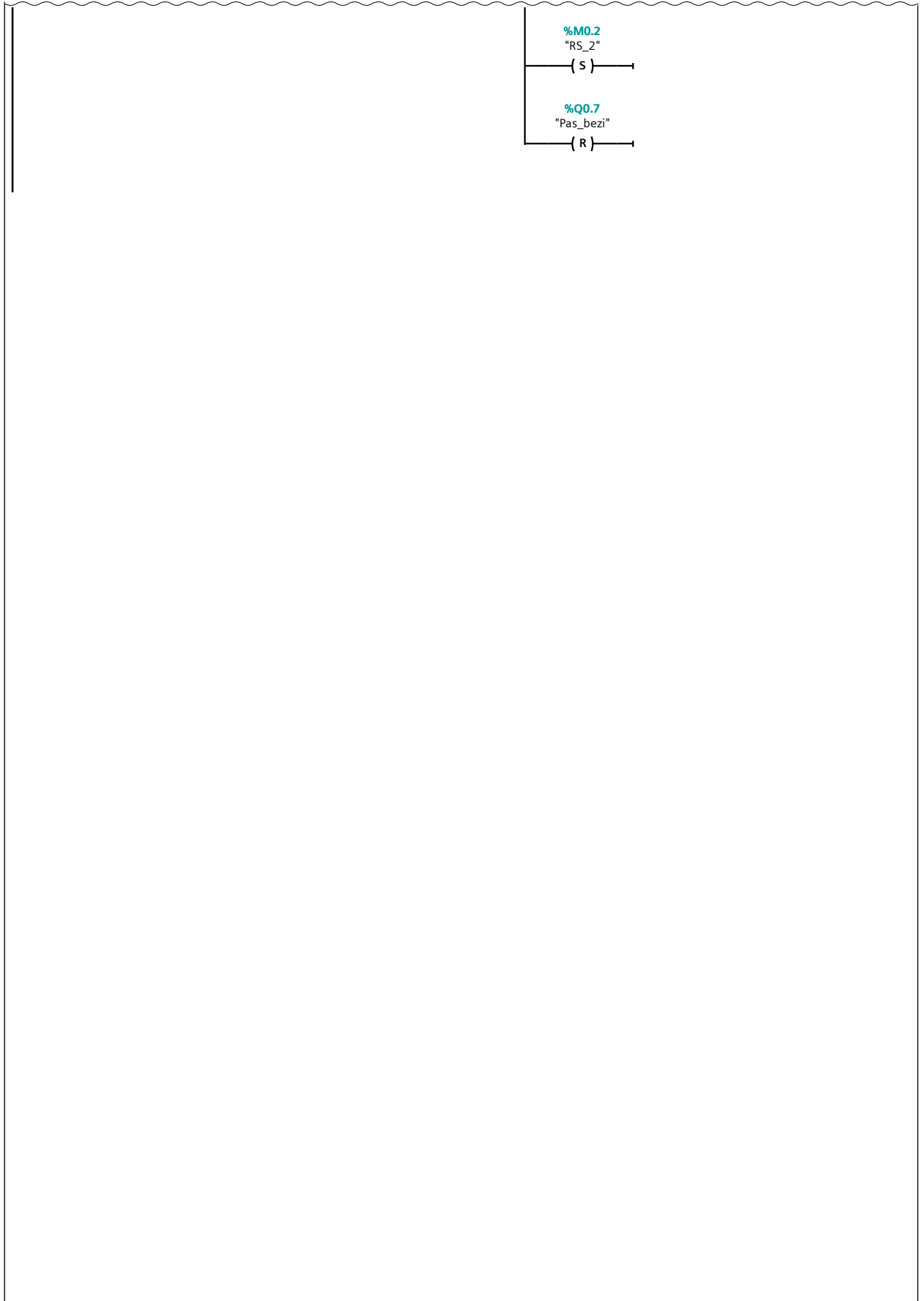
Kombinace M0.0, M0.1, M0.2: 000 běží pás, 100 plnění vínem, 010 korkování, 110 kapsle, 001 etiketa

Network 1: (1.1 / 2.1)



Network 1: (2.1 / 2.1)

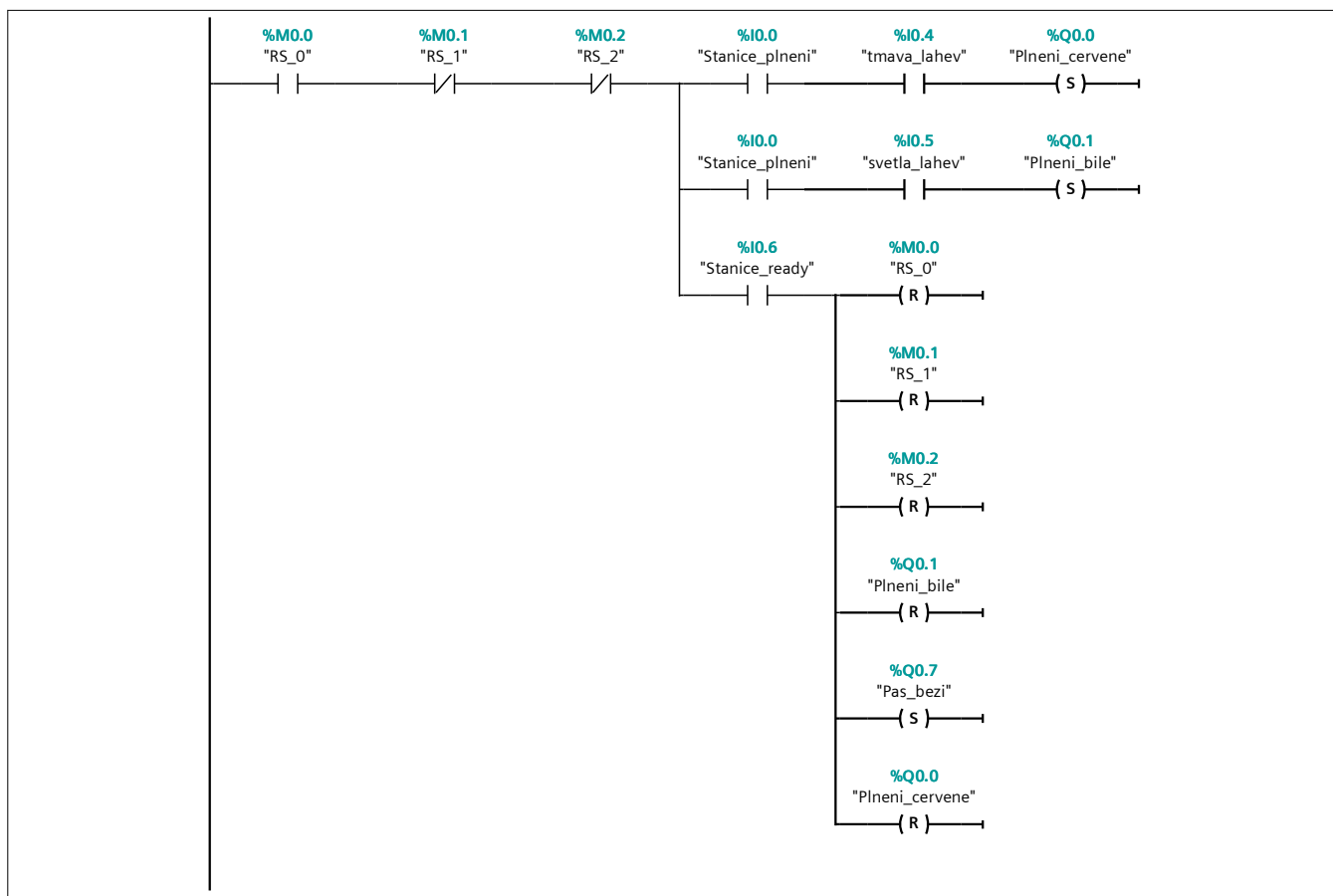
1.1 (Page5 - 2)



Symbol	Address	Type	Comment
"RS_0"	%M0.0	Bool	
"RS_1"	%M0.1	Bool	
"Stanice_plneni"	%I0.0	Bool	
"RS_2"	%M0.2	Bool	
"Stanice_korkovani"	%I0.1	Bool	
"Stanice_kapslovani"	%I0.2	Bool	
"Stanice_etiketovani"	%I0.3	Bool	
"Pas_bezi"	%Q0.7	Bool	

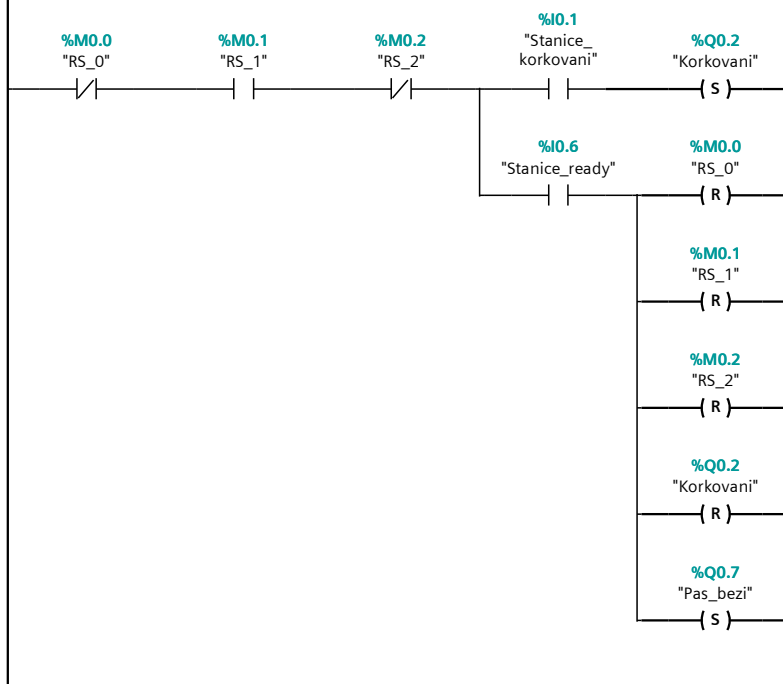
Network 2:

Stav plnění vhodné lahve vínem - lahev červená - červené, lahev bílá- bílé



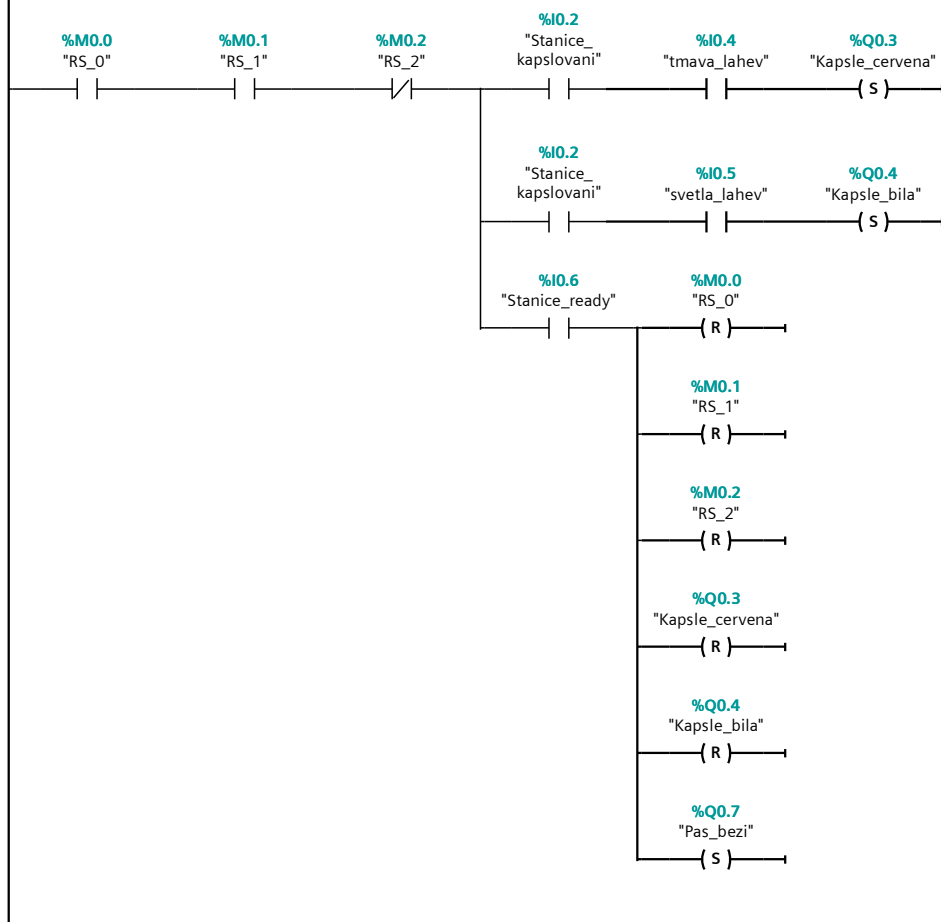
Symbol	Address	Type	Comment
"RS_0"	%M0.0	Bool	
"RS_1"	%M0.1	Bool	
"Stanice_plneni"	%I0.0	Bool	
"RS_2"	%M0.2	Bool	
"tmava_lahev"	%I0.4	Bool	
"svetla_lahev"	%I0.5	Bool	
"Stanice_ready"	%I0.6	Bool	
"Pas_bezi"	%Q0.7	Bool	
"Plneni_bile"	%Q0.1	Bool	
"Plneni_cervene"	%Q0.0	Bool	

Network 3:



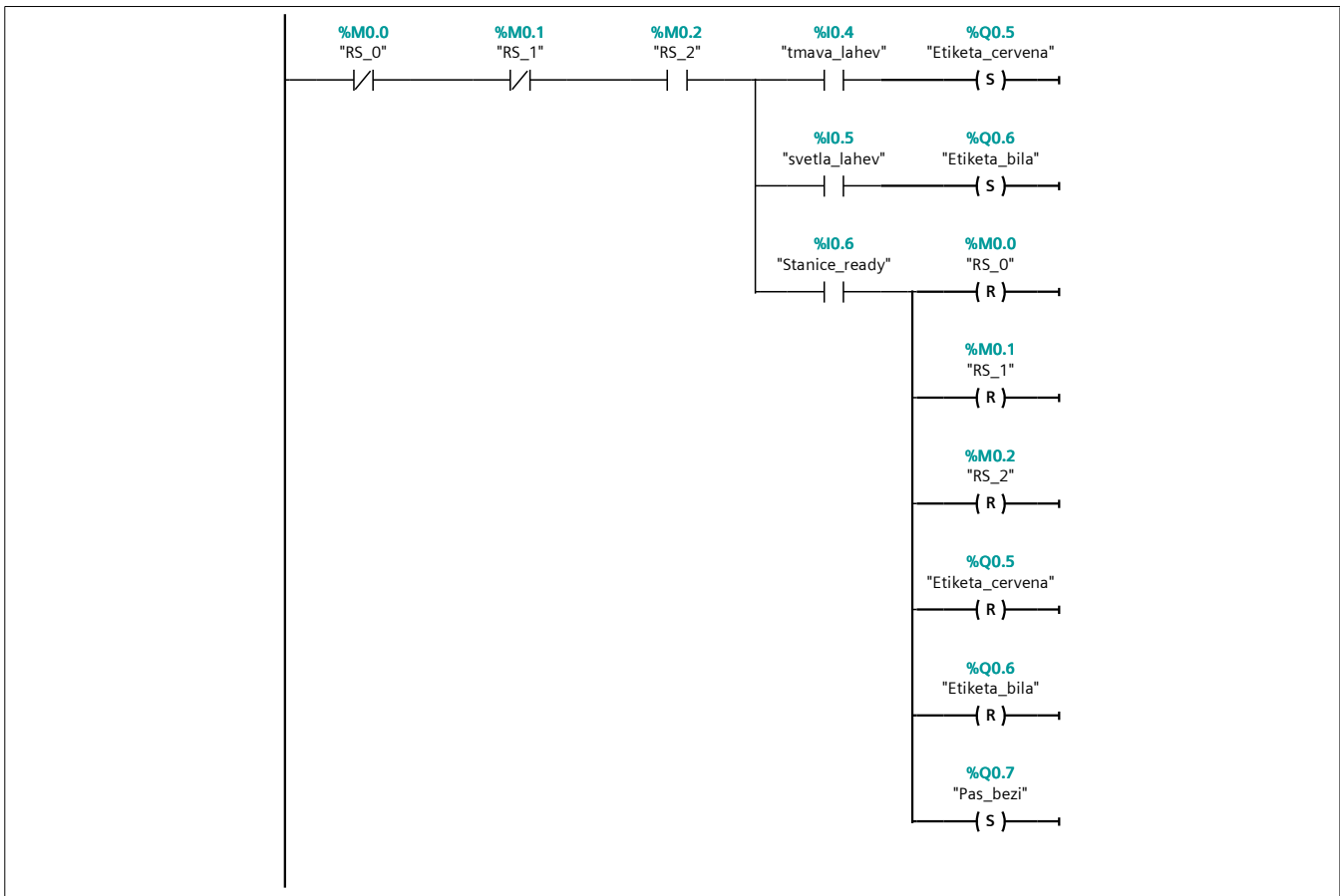
Symbol	Address	Type	Comment
"RS_0"	%M0.0	Bool	
"RS_1"	%M0.1	Bool	
"RS_2"	%M0.2	Bool	
"Stanice_korkovani"	%IO.1	Bool	
"Stanice_ready"	%IO.6	Bool	
"Korkovani"	%Q0.2	Bool	
"Pas_bezi"	%Q0.7	Bool	

Network 4:



Symbol	Address	Type	Comment
"RS_0"	%M0.0	Bool	
"RS_1"	%M0.1	Bool	
"RS_2"	%M0.2	Bool	
"Stanice_kapslovani"	%I0.2	Bool	
"tmava_lahev"	%I0.4	Bool	
"svetla_lahev"	%I0.5	Bool	
"Stanice_ready"	%I0.6	Bool	
"Kapsle_cervena"	%Q0.3	Bool	
"Pas_bezi"	%Q0.7	Bool	
"Kapsle_bila"	%Q0.4	Bool	

Network 5:



Symbol	Address	Type	Comment
"RS_0"	%M0.0	Bool	
"RS_1"	%M0.1	Bool	
"RS_2"	%M0.2	Bool	
"tmava_lahev"	%I0.4	Bool	
"svetla_lahev"	%I0.5	Bool	
"Stanice_ready"	%I0.6	Bool	
"Pas_bezi"	%Q0.7	Bool	
"Etiketa_cervena"	%Q0.5	Bool	
"Etiketa_bila"	%Q0.6	Bool	

PLC_1 [CPU 314C-2 DP]



















Technology objects

This folder is empty.

PLC_1 [CPU 314C-2 DP] / PLC tags / Default tag table [18]

PLC tags

PLC tags

Name	Data type	Address	Retain	Visible in HMI	Accessible from HMI	Comment
 RS_0	Bool	%M0.0		True	True	
 RS_1	Bool	%M0.1		True	True	
 Pas_bezi	Bool	%Q0.7		True	True	
 Stanice_plneni	Bool	%I0.0		True	True	
 RS_2	Bool	%M0.2		True	True	
 Stanice_korkovani	Bool	%I0.1		True	True	
 Stanice_kapslovani	Bool	%I0.2		True	True	
 Stanice_etiketovani	Bool	%I0.3		True	True	
 tmava_lahev	Bool	%I0.4		True	True	
 Plneni_cervene	Bool	%Q0.0		True	True	
 svetla_lahev	Bool	%I0.5		True	True	
 Plneni_bile	Bool	%Q0.1		True	True	
 Stanice_ready	Bool	%I0.6		True	True	
 Korkovani	Bool	%Q0.2		True	True	
 Kapsle_cervena	Bool	%Q0.3		True	True	
 Kapsle_bila	Bool	%Q0.4		True	True	
 Etiketa_cervena	Bool	%Q0.5		True	True	
 Etiketa_bila	Bool	%Q0.6		True	True	

PLC_1 [CPU 314C-2 DP] / PLC tags / Default tag table [18]

User constants

User constants

Name	Data type	Value	Comment
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PLC_1 [CPU 314C-2 DP]

PLC data types

This folder is empty.

PLC_1 [CPU 314C-2 DP] / Watch and force tables

Force table

Name	Address	Display format	Force value	Comment
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PLC_1 [CPU 314C-2 DP] / PLC alarms

PLC alarms

PLC alarms

no entries

PLC_1 [CPU 314C-2 DP] / PLC alarms

User diagnostics alarms

User diagnostics alarms

no entries

PLC_1 [CPU 314C-2 DP] / PLC alarms

System diagnostics alarms

System diagnostics alarms

no entries

PLC_1 [CPU 314C-2 DP]

Text lists

This folder is empty.

PLC_1 [CPU 314C-2 DP] / Local modules

CP 343-2_1 [CP 343-2]

CP 343-2_1

General

Name	CP 343-2_1	Author	ucitel
Comment		Rack	0
Slot	4		

General\Catalog information

Short designation	CP 343-2	Description	Firmware V3.0/V3.1. Basic module for AS-i attachment. Support of AS-i A/B slaves and AS-i-7.3/7.4 analog slaves.
Order number	6GK7 343-2AH01-0XA0	Firmware version	V3.1

I/O addresses\input addresses

Start address	256	End address	271
Process image	None		

I/O addresses\Output addresses

Start address	256	End address	271
Process image	None		

Anchor (AsiCMAAddressesOverviewMenu)

I address	O address	AS-i address
		0
256.0 ...256.3	256.0 ...256.3	1A
257.4 ...257.7	257.4 ...257.7	2A
257.0 ...257.3	257.0 ...257.3	3A
258.4 ...258.7	258.4 ...258.7	4A
258.0 ...258.3	258.0 ...258.3	5A
259.4 ...259.7	259.4 ...259.7	6A
259.0 ...259.3	259.0 ...259.3	7A
260.4 ...260.7	260.4 ...260.7	8A
260.0 ...260.3	260.0 ...260.3	9A
261.4 ...261.7	261.4 ...261.7	10A
261.0 ...261.3	261.0 ...261.3	11A
262.4 ...262.7	262.4 ...262.7	12A
262.0 ...262.3	262.0 ...262.3	13A
263.4 ...263.7	263.4 ...263.7	14A
263.0 ...263.3	263.0 ...263.3	15A
264.4 ...264.7	264.4 ...264.7	16A
264.0 ...264.3	264.0 ...264.3	17A
265.4 ...265.7	265.4 ...265.7	18A
265.0 ...265.3	265.0 ...265.3	19A
266.4 ...266.7	266.4 ...266.7	20A
266.0 ...266.3	266.0 ...266.3	21A
267.4 ...267.7	267.4 ...267.7	22A
267.0 ...267.3	267.0 ...267.3	23A
268.4 ...268.7	268.4 ...268.7	24A
268.0 ...268.3	268.0 ...268.3	25A
269.4 ...269.7	269.4 ...269.7	26A
269.0 ...269.3	269.0 ...269.3	27A
270.4 ...270.7	270.4 ...270.7	28A
270.0 ...270.3	270.0 ...270.3	29A
271.4 ...271.7	271.4 ...271.7	30A
271.0 ...271.3	271.0 ...271.3	31A